

# Abstracts

## Low cost MMIC DBS chip sets for phased array applications

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*J. Wallace, H. Redd and R. Furlow. "Low cost MMIC DBS chip sets for phased array applications." 1999 MTT-S International Microwave Symposium Digest 99.2 (1999 Vol. II [MWSYM]): 677-680 vol.2.*

The design and performance of two different chip sets for 11.7 to 12.7 GHz DBS receive phased arrays are described. Both chip sets exhibit state of the art noise figure and phase shift performance; are extremely compact and power efficient; and are of very high yield to minimize cost.

 [Return to main document.](#)